

REMARKS

This amendment is filed in response to the Notice of Non-Compliant Amendment dated May 17, 2006. A complete listing of all the claims is presented herein.

Claim 8 is pending in the present application. Claim 8 was rejected under 35 U.S.C. §102(e) as being anticipated by *Tiedmann Jr. et al.* (U.S. 6,246,673).

Please amend Claim 8 as set forth herein. No new matter has been added.

Tiedemann, Jr. et al. discloses a method and system for handoff between an asynchronous CDMA base station and a synchronous CDMA base station.

Claim 8 has been amended to include a first phase locked loop (PLL) for supplying a carrier frequency signal to the async demodulation module; a second PLL for supplying a carrier frequency signal to the sync demodulation module, a local generator for supplying a local oscillation signal based on a reference clock provided from the async demodulation module and sync demodulation module to the first PLL and the second PLL, wherein said async demodulation module comprises a first radio frequency section and a first baseband processor, said sync demodulation module comprises a second radio frequency section and a second baseband processor, and said switch comprises a first switch and a second switch.

Additionally, Claim 8 has been amended to recite the following elements: said first switch operates to connect the received signal to one of the async demodulation module and the sync demodulation module, said second switch operates to connect the reference clock to a local generator; said first radio frequency section multiplies a received signal from the async base station via the first switch by a carrier frequency signal from a first phase locked loop (PLL) to perform frequency down-conversion to a baseband signal, said first baseband processor demodulates the baseband signal from the first radio frequency section by despreading; said second radio frequency section multiplies the received signal from the async base station via the

first switch by a carrier frequency signal from a second phase locked loop (PLL) to perform frequency down-conversion to a baseband signal; and, said second baseband processor enables the mobile station to operate according to the timing of the sync base station acquired for a given time period during communication with the async base station, said second baseband processor includes a PN generator continues even while the mobile station is in communication with the async system, and said second baseband processor demodulates the baseband signal from the second radio frequency section by despreading.

It is respectfully submitted that Tiedemann, Jr. et al. does not teach or disclose any of the above elements of amended Claim 8.

Based on at least the foregoing, withdrawal of the rejection of Claim 8 is respectfully requested.

Accordingly, all of the claims pending in the Application, namely, Claim 8, is believed to be in condition for allowance. Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicants' attorney at the number given below.

Respectfully submitted,



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